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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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Serial No.: 10/748,559

Filed: December 29, 2003

For: A CMOS DEVICE WITH
METAL AND SILICIDE GATE
ELECTRODES AND A
METHOD FOR MAKING IT

Art Unit: 2811

Examiner: unknown

Attorney Docket: P18244

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT


Sir:

This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the references cited on that form. It is respectfully requested that the cited references be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement constitutes prior art or is otherwise material to patentability.

Respectfully submitted,

Dated: 9-17-2004



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FIRST CLASS CERTIFICATE OF MAILING
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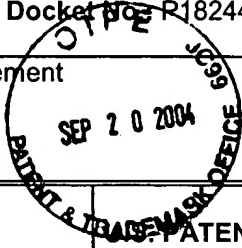
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9-17-2004
Date

Form PTO-1449 (Modified)		Atty Docket No. R18244		Serial No.: 10/748,559	
List of Patents and Publications Statement (Use several sheets if necessary)				Applicant: Justin K. Brask et al.	
				Filing Date: December 29, 2003	



REFERENCE DESIGNATION						
Examiner Initials	Document No.	Class	Sub-Class	Filing date if appropriate		
	AA	6,410,376 B1	Ng et al.	438	199	
	AB	6,586,288 B2	Kim et al.	438	183	
	AC	US2002/0058374 A1	Kim et al.	438	228	
	AD					
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FOREIGN PATENT DOCUMENTS							
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		
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